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S/N 10/829.559

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Asher Hazanchuk et al.

Examiner: Do, Chat C

Serial No.:

10/829,559

Group Art Unit: 2193

Filed:

April 22, 2004

Docket No.: ALT.P030 (A01252)

Title:

METHOD AND APPARATUS FOR IMPLEMENTING A MULTIPLIER UTILIZING

DIGITAL SIGNAL PROCESSOR BLOCK MEMORY EXTENSION

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT

Dear Sir:

In response to the Office Action mailed on September 10, 2007, Applicants respectfully request the Examiner to consider the following amendments. A petition for a one month extension of time is being submitted herewith.

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